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Drafted by:

### 1. Product Overview

In 2022, Actionpower launched the fourth-generation A series regenerative AC source & load integrated panel, which has both the function of grid simulation and the AC load simulation to meet various tests in the green energy industries. The source mode can simulate both normal and abnormal characteristics according to international regulation; the load mode can simulate the linear RLC load, the rectified load and the switching load, thereby filling the market gap of high power AC electronic load.

The A series feedback AC source & load integrated panel has the matrix parallel capability, which can realize the multi-station independent operation in laboratory and improve the utilization efficiency of power supply. Actionpower has provided the third-party testing organization with four-station new energy test platform with capacity up to 6MW, which is the largest low-voltage test system in China.

In the industries related to green energy, such as PV inverter, energy storage system ESS/power conversion system PCS, micro-grid, onboard charger OBC/BOBC, high power charging pile, and uninterrupted power supply UPS, the combination of AC power supply and AC load functions in the product test helps customers to reduce procurement costs. Compared with the traditional reactive load, the equipment feeds the power back to the grid, which is more in line with the needs of the low-carbon society. It once again leads the development direction of the new generation high 一方市 ACTIONPOWER power AC power supply.





2. Selection	Selection								
Model	Power Voltage Range (kVA) (V) @L-N		Frequency Range (Hz)	quency ge (Hz) (Maximum Current(A) @Three-phase		Size (mm) W x H x D			
AGL-30-4505	300	0-450	40-70	454	2930	2900×1950×1200			
AGL-40-4506	400	0-450	40-70	606	3220	2900×1950×1200			
AGL-50-4508	500	0-450	40-70	757	4360	4400×1950×1400			
AGL-60-4509	600	0-450	40-70	910	4910	4400×1950×1400			
AGL-75-4512	750	0-450	40-70	1136	6160	4400×1950×1400			
AGL-100-4516	1000	0-450	40-70	1515	8720	8800×1950×1400			
AGL-30-7003	300	0-700	40-70	286	2860	2900×1950×1200			
AGL-40-7004	400	0-700	40-70	380	3040	2900×1950×1200			
AGL-50-7005	500	0-700	40-70	476	3260	2900×1950×1200			
AGL-60-7006	600	0-700	40-70	572	4170	4400×1950×1200			
AGL-75-7007	750	0-700	40-70	714	5880	4400×1950×1400			
AGL-100-7010	1000	0-700	40-70	1000	6610	4400×1950×1400			
AGL-H30-8603	300	0-860	40-70	286	2860	2900×1950×1200			
AGL-H40-8604	400	0-860	40-70	380	3040	2900×1950×1200			
AGL-H50-8605	500	0-860	40-70	476	3260	2900×1950×1200			



#### 西安爱利塞博电气股份有限公司 XI'AN ACTIONPOWER ELECTRIC CO.,LTD. Drafted by: **Classification:** Public 4170 AGL-H60-8606 600 40-70 4400×1950×1200 0-860 572 AGL-H75-8607 40-70 5880 4400×1950×1400 750 0-860 714 AGL-H100-8610 1000 1000 4400×1950×1400 0-860 40-70 6610

## 3. Advantages and Functions of AC Load



ERE

# 3.1 Linear Load Characteristic Simulation

The A series feedback AC source & load integrated panel has 5 built-in RLC network models, which can flexibly adjust the parameters to simulate the linear load characteristics, in order to fully validate the product performance in different impedance modes.





Voltage amplitude change in CC mode

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CP mode: voltage amplitude transient change CF=2.5





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#### 3.3 The 1ms dynamic in load mode



With various types of waveform, such as triangular wave, sine wave, square wave and clipping wave, the user can call the waveform from the menu and preview the selected waveform on the screen.

X ※音和意情 ACTIONPOWER 写和意情 ACTIONPOWER ※ 着在TIONPOW **《**岩和意情



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- 设备信息	设备调试 稳态参数 暂态List 谐波发生 自定义波形 高级设置 故障记录	
- AGS	波形编辑 波形选择	
设备调试	自定义波形编号 USER01 V 下载波形 编辑波形 导入 导出	
暂态List	配置	^
增波发生 自定义波形	波形英型 C[%]	
高级设置	削顶波 - 50	
故障记录		^
	日川以政	
	-1	
	-1.5 0 +180 +Edition	360
	ी पार्थ []	
ACTION	POWER ACTO	
	Clipping wave selection interface	ACTIONPO

Waveform editing in customized mode can reproduce the real source waveform on site.

3					-X	言	ONPOI	NER					
➡ ▷ 开机	Q [分 10] 日本 关邦 温位 放电 佐夏出「役置 用新		<b>)</b> מ								系统状态: 输出状态:输出吸合	源载模式: 通信状态:故障	☑ I 8 - ₽ × ※ 岩和意情
<ul> <li>■ 设备信息</li> <li>■ AGS</li> </ul>	<ul> <li>设备调试 稳态参数 暫态List 當波发生</li> <li>波形编辑 波形选择</li> </ul>	自定义波形高	级设置 故障	\$°记录									
稳态参数 设备调试	自定义波形编号 USER01 · 下载波形	编辑波形	导入	导出									
智态List	配置	波形编辑							- 🗆	×			^
自定义波形高级设置		0	0.00153 1	0.00307 2	0.0046 3	0.00614 4	0.00767 5	0.0092 6	0.01074 7	÷			
故障记录	被形	0.01227	0.01381	0.01534	0.01687	0.01841	0.01994	0.02147	0.02301	-			^
		8	9	10	11	12	13	14	15				
	15 1 1 0 0 1 1 1 1 1 1	0.02454 16 0.03681 24 0.04907	0.02607 17 0.03834 25 0.0506	0.02761 18 0.03987 26 0.05213	0.02914 19 0.04141 27 0.05366	0.03067 20 0.04294 28 0.0552	0.03221 21 0.04447 29 0.05673	0.03374 22 0.046 50 0.05826	0.03527 23 0.04754 81 0.05979				
	-1.5 0	0.06132 40	0.06285 41	0.06438	0.06591 43	1 44	0.06897 45	0.0705 46	0.07203 47				360
		0.07356 48	0.07509 49	0.07662 50	0.07815 51	0.07968 52	0.08121 53	0.08274 54	0.08427 55				
		0.0858 56	0.08733 57	0.08885 58	0.09038 59	0.09191 60	0.09344 61	0.09496 62	0.09649 63				
		0.00000	0.0005.4	0 10107	0 1076	0 10412	0 10565	0 10717	0 1007	*			
			1										

Customization edition interface



The power supply allows to synthesis 2-50 times of harmonics with 50Hz or 60Hz fundamental frequency, and can be applied for the tests under IEC61000-3-2/3-12.



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#### 4.2 Inter-harmonic

The power supply can synthesis the 1Hz-3000Hz inter-harmonics to form the distorted waveform of output voltage.



Inter-harmonic Setting Interface

Inter-harmonic Synthesis Waveform

目夏国 Starting frequency, ending frequency, interval and the like can be set for inter-harmonics to test the inter-harmonic sweep frequency, so as to be applied to the tests of IEC 61000-4-13 standard.



The flicker level can be directly set for power supply and the flicker characteristics of the power grid can be easily simulated to test the flicker adaptability of the test object.



Flicker simulation

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### 4.4 High/Low Voltage Ride Through

The single-phase, two-phase, and three-phase high/low voltage ride through tests can be performed for the power supply. The trigger phase angles of the ride through points can be set for the power supply to meet the requirements of tests under various standards. The minimum setting voltage of power supply is less than 5V, and the rise/fall time is 1ms.



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The trigger phase angle of ride through point is settable to meet the fault ride through test requirements of different countries.



Phase A, B and C can be changed at the same time for low voltage ride through, phase B and C can be switched for low voltage ride through, so as to meet the tests of VDE-AR-N 4105 regulations.



The power supply can set the unbalanced voltage, phase and other information to make the power supply into an unbalanced state, and automatically display the unbalance factor. It can also directly set the unbalance factor, automatically calculate and output the voltage, phase and other information under the unbalance factor, easily simulate the grid unbalance characteristics, and be used to test the adaptability of the voltage unbalance degree of the tested

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#### object.



Built-in 27 kinds of typical harmonic voltage waveform help users to take one with one click.





1ms interruption waveform

Z 4.00ms

250 V

250 V

250 V

3

50.0 V

22 4月 2022

11:40:33

1.25M次/秒

5M 点



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#### 4.8 Universal Programmable

All AGL modes feature 100-step nested programming, with the entire programming loopable for 999 times. The output voltage, frequency, and phase can be programmed and output according to the customer's requirements. The multi-sequence complex output modes can be combined by variation time, holding time, programming steps, cycle times, etc. for tests under complex conditions and can be used for tests of over & under voltage, over & under frequency, etc. Programming data has memory function and supports import and export. The power supply can be continuously programmed within the output range with no distortion of waveform. Additionally, there is control of low-voltage trigger signal electrically isolated from other parts of the device in output, and such signal is always synchronized with the variation of power output parameters, single-step, single-cycle, single-trigger.



LIST mode setting interface

Te<u>k 预览</u>

1 200 V 3 200 V

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■ ▶ 开机	关机		切 位 株复出		<b>〕</b> (新	行う物定	<b>~</b> 解物		系统状态: 输出状态:		源载模式: 通信状态:故	?			
□ 设备信息	稳态参	数 自定	义波形 暫志	sPulse 🕈	雪态List 音	态Step	间谐波 🛛	内变 三相	不平衡	谐波发生	设备调试	高级设置	故障记录		
G AGS	加载	触发	结束	导入	导出	读取	存储	实例: : :	实例1						
稳态参数	状态													^	
设备调试 暂态List	时间	0	h	min 0.	s s	<u> </u> 進度					0 %				
暂态Pulse	编程配置	6												^	
暫态Step 谐波发生 间谍波	循环次3 1	BX	结束状态 退出 🗸 🗸	有效值模式 自动	<ul><li> 触发描 </li><li> 単次 </li></ul>	出	触发模式 自动 💙	触发源 本地软件	~	相数选择 三相独立	耦合 ~ AC	方式 ~			
问唱成	基波													~	
三相不平衡		波形	Uac[V]	相位[°]	频率[Hz]	相角[°]									
自定义波形	Ø1	正弦波	10	0											
高级设置	Ø2	正弦波	10	240	50	5									
故障记录	Ø3	正弦波	10	120											
NF	脉冲													^	
		波形	Uac[V]	相位[°]	频率[Hz]	周期[s]	脉冲宽度[s	]							
	Ø1	正弦波	10	0											
	Ø2	正弦波	10	240	50	0.5	0.02								
	Ø3	正弦波	10	120											

#### PULSE mode setting interface M 400ms Te<u>k 预览</u> 4 缩放位置: 2.70。 宿放系数:40 X i POP 17 12月2019 19:09:29 250k次/秒 1M 点 16.0 V 17 12月2019 19:10:33 250k次/秒 1M 点 16.0 V 1 200 V 3 200 V Z 10.0ms 2 200 V 4 2.00 V ACI User-defined Waveform Voltage Phase Change FLAG Tek预览 M 400ms



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### 5. Technical Specification

5.1 Specification in Source	Mode SETIONPOWER						
14	一個						
	Source Mode						
Category	ACT						
Basic Parameters							
Number of output phases	Three-phase three-wire ABC or three-phase four-wire ABCN						
Load power factor	-1~+1						
AC output							
Voltage							
Resolution (V)	0.01						
Accuracy	±0.1% F.S.						
Types of waveform	Sine						
DC component (mV)	<50						
	<0.5% @50Hz/60Hz >=220V no-load						
Voltage distortion	<1% @Linear load						
voltage distortion	<1.0%@40Hz~70Hz >=220V no-load						
	< 1.5% @ Linear load						
Load regulation rate	±0.1% F.S						
Line regulation rate	±0.1% F.S. @10% variation						
Voltage slew rate	AC>1.0V/µs						
Dynamic response	<1ms (10%-90%Umax)						
Three-phase unbalance	Negative sequence voltage less than 1%, short-time less than 2%						
Frequency							
Resolution (Hz)	0.001						
Accuracy	±0.01% or 0.005Hz, take the greater of the two						
Phase							
Scono	A = 0°, B = -120°, C = 120° (default); programmable range: 0°–359.9°, independently adjusta						
Scope	three-phase						
Accuracy	±0.3°						
Resolution	0.1°						
Harmonics							
Times	50 times @50 , 50 times @60 , below nominal voltage						
	Maximum 40% for 2-10 single harmonics, and no more than 40% for 2-10 total harmonics;						
Component	Maximum 20% for 11-20 single harmonics, and no more than 20% for 11-20 total harmonics;						
	Maximum 10% for 21-30 single harmonics, and no more than 10% for total harmonics;						
	Maximum 5% for 31-50 single harmonics, and no more than 5% for total harmonics;						

Drafted by: **Classification:** Public Amplitude error ±5%@ harmonic content setting value below 25 times 0°-359.9° Phase angle range **Preview function** Harmonic synthesis waveform can be previewed Editing mode Import, export, read, storage **Inter-harmonic** 1Hz-3,000Hz, content <10% Frequency range 三形属 Programming steps 100 steps Component, start frequency, end frequency, step length, time of execution, interval and cycles **Programming parameters** Editing mode Add, delete, import, export, store, read **Flicker** Flicker level 1.0-10.0, totally 10 levels in total, and one-key call Adjustment step length 1 Accuracy ±0.2 Preview of flicker trend chart **Preview function** Three-phase unbalance simulation Three-phase voltage, phase; unbalance factor; Adjustment mode Unbalance factor adjustment 1~100, one-key call range (%) Unbalance factor adjustment 1 step length (%) Accuracy (%) ±0.5% High & Low Voltage Ride Through Mode LVRT / HVRT / H&LVRT Configurations Voltage, frequency, phase, rise time, hold time, trigger phase angle, and trigger pulse output Regulations GB/T, VDE-AR-N 4105, NRS 097-2-1, G83, and EN50438 standards. Programmable Programming steps 100 steps **Programming parameters** Voltage, frequency, phase, change time, hold time, trigger phase angle, and trigger pulse output Up time range 100µs-999s Flat top time range 100µs-999s Minimum programming time 100µs step Editing mode Import, export, store, read **Relevant functions** Three-phase unbalance, fall, interruption, HVRT,LVRT etc. Operation mode Operation, stop, cycling Trigger mode Automatic, manual, external Measurement **Output Voltage** Resolution (V\_rms) 0.01 ±0.1% F.S. Accuracy **Output frequency** 

		Drafted by:	Classification: Public
Resolution ( <i>Hz</i> )		0.001	
Accuracy		±0.01%	
Output Current			
Resolution (A)		0.1	
Accuracy		±0.2% F.S.	
Active power			
Resolution (W)		1	
Accuracy		±0.3% F.S.	
Apparent power			
Resolution (VA)		1	
Accuracy		±0.3% F.S.	
Power factor			
Scope		-1.00~+1.00	
Resolution		0.001	
Power supply mode			
Wiring mode		Three-phase four-wire ABC+PE	
Frequency (Hz)		47 - 63	
Voltage range (V)	一章围	±15% @380V L-L	一一天日夏
Power factor	* FILDNPOWER	0.99 @ above half load	ACTIONPO
Efficiency	VA V	Models of 300kW and above: > 94%	
Enciency		••••••••••••••••••••••••••••••••••••••	
Harmonic current		≤3%	山南
Others			
Communication interface	Sta	ndard configuration: LAN; optional: RS48	5, CAN
External interlocking	External interlocking input	is normally on/off; external interlocking o	utput is normally on/off.
Trigger signal	ACTIONPOW	Trigger input/output	AC
Insulation and withstanding		$10M\Omega/DC500V;$ $3600VAC/1min$	
voltage		* FILLSOWER	
Control and display	Local touch screen contr	ol, remote computer control, display vol	age, current, frequency and
control and display	和意愿	power 💉	ETIONPOWER
Cooling mode	CTIONPOWE	Fan cooling	P
	※ 着い	<b>日美唐</b> IONPOWER	
5.2 Specification in Load N	lode	※完而意應 ACTIONPOWER	bE
	一個		

	Load mode	ACTIONPOW
Basic Parameters		
Load wiring	Three-phase three-wire ABC or thre	e-phase four-wire ABCN
Load mode		

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	CC: Current, power factor, load type, current slope, output waveform								
	CP: Apparent power, power factor, load type, power slope, output waveform								
Linear load	CR: Resistance value								
	RLC: RLC connection mode R+L+C, R//L//C, (R//C)+L, (R+L)//C, (R+C)//L								
	Resistance value, inductance value and capacitance value can be set								
Nonlinear load	Current, power, peak factor, current slope, power slope								
Zara valtaga start	Simulate on-load start, switch seamlessly to CC or CP after start, with configurable switching								
	condition								
Voltage									
Voltage distortion limit	Uthd <10% below 20 times								
Current									
Resolution (A)	0.01								
Accuracy (A)	±0.2% F.S. @ CC mode								
Waveform	Sine, square wave, triangular wave, clipping wave, customized waveform								
Current distortion	<2%@50Hz@ full load under rated voltage								
Current slew rate	10%~90% nominal current > 1A/us								
Dynamic response	< 1ms@10%~90% nominal current								
Frequency									
Range (Hz)	40.00 - 70.0								
Accuracy	$\pm 0.01$ Hz								
Harmonics									
Harmonic order	50 times								
	Max 40% for 2-10 single harmonics, and no more than 40% for 2-10 total harmonics;								
Limit of each order	Maximum 20% for 10-20 single harmonics, and no more than 20% for 10-20 total harmonics;								
	Maximum 10% for 21-30 single harmonics, and no more than 10% for total harmonics;								
	Maximum 5% for 31-50 single harmonics, and no more than 5% for total harmonics;								
Phase angle of each order	0°- 359.9°								
Power									
Resolution (VA)									
Accuracy (VA)	±0.5% F.S. @ CP mode								
Power factor									
Power factor	-1-1 (resistance inductance, resistance capacity and current direction can be set)								
Resolution	ACTIONPOWER 0.01								
Crest factor									
Scope	1.414-4 INPOWER								
Resolution	0.001								
List programming									
Programmable steps	100 steps								
Supported modes	CC, CP								
Drogrammable agreenter	Current/power, power factor, load type, change time, holding time, trigger phase angle, trigger								
Programmable parameters	pulse output								

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UP time range	100µs-999s
Flat top time range	100µs-999s
Minimum programmable time	100.05
step	100μ3
Editing mode	Add, delete, store, and read
Operation mode	Operation, stop, cycling
Trigger mode	Automatic, manual, external
Measurement	
AC voltage	
Resolution (V)	0.01
Accuracy	±0.1% F.S.
Output frequency	
Resolution (Hz)	0.001
Accuracy	±0.01Hz
AC current	
Resolution (A)	0.1
Accuracy	±0.2% F.S.
Active power	
Resolution (W)	1
Accuracy	±0.5% F.S.
Apparent power	
Resolution (VA)	1
Accuracy	±0.5% F.S.
Power factor	
Scope	-1.00~+1.00
Resolution	0.001
Crest factor	
Scope	1.414-4
Resolution	0.001
Power mode	
Wiring mode 🕺	Three-phase four-wire ABC+PE
Frequency (Hz)	47 - 63
Voltage range (V)	±15% @380V L-L
Power factor	0.99@100%
Efficiency	> 0.93 TIONPOWER
Harmonic current	≤3%
Others	
Communication interface	Standard configuration: LAN; optional: RS485, CAN
External interlocking	External interlocking input is normally on/off; external interlocking output is normally on/off.
Trigger signal	Trigger input/output
Insulation and withstanding	10MΩ/DC500V; 3600VAC/1min

Drafted by:

